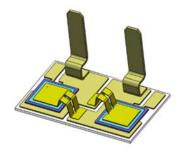




# **DBC056C/16KQ**

### **Description**

- 1) Components adopt vacuum welding to well control void and rated voltage up to 1600V.
- 2) A package of two inverse parallel SCRs.
- 3) Thyristor chips are welding on the ceramic copper clad laminate, products with high electricity ability, excellent heat dissipation ability.



## **Typical Application**

Constant temperature system, CNC machine, remote control system, lighting control, power compensation and so on.

### **Absolute Maximum Ratings** (Packaged into modules, unless otherwise specified, T<sub>CASE</sub>=25°C)

Parameter	Test Conditions	Symbol	Values	Unit
Operating junction temperature range		TJ	-40~+125	${\mathbb C}$
Repetitive peak off-state voltage	TJ=25℃	V <sub>DRM</sub>	1600	V
Repetitive peak reverse voltage	TJ=25℃	V <sub>RRM</sub>	1600	V
Non-repetitive peak off-state voltage	TJ=25°C	V <sub>DSM</sub>	1700	V
Non-repetitive peak reverse voltage	TJ=25°C	V <sub>RSM</sub>	1700	V
Average on-state current	Tc=80°C	I <sub>T(AV)</sub>	56	А
RMS on-state current	Tc=80°C	I <sub>T(RMS)</sub>	90	А
Non-repetitive surge peak on-state current	t <sub>P</sub> =10ms	I <sub>TSM</sub>	1120	А
I <sup>2</sup> t value for fusing	t <sub>P</sub> =10ms	l <sup>2</sup> t	6200	A <sup>2</sup> s
Critical rate of rise of on-state current	IG=2×IGT	di/dt	150	A/µs

### **Electrical Characteristics** (Packaged into modules, unless otherwise specified, T<sub>CASE</sub>=25°C)

Parameter	Test Conditions	Symbol	Values	Unit
Peak on-state voltage	I <sub>TM</sub> =168A,t <sub>P</sub> =380μs	Vтм	≤1.8	V
	V <sub>D</sub> =V <sub>DRM</sub>			
Repetitive peak off-state current	Tc=25℃	I <sub>DRM1</sub>	≤50	μΑ
	Tc=125℃	I <sub>DRM2</sub>	≤10	mA

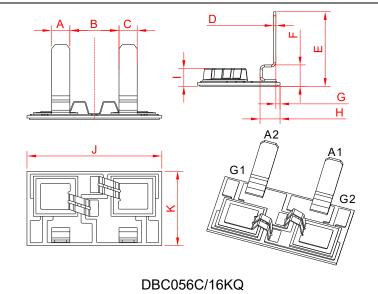


# **Solid DBC Modules**

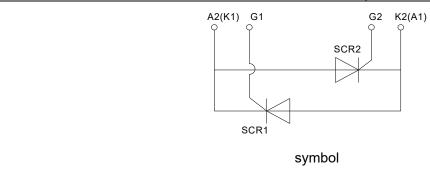
	V <sub>R</sub> =V <sub>RRM</sub>			
Repetitive peak reverse current	Tc=25℃	I <sub>RRM1</sub>	≤50	μΑ
	Tc=125℃	I <sub>RRM2</sub>	≤10	mA
Triggering gate current	V <sub>D</sub> =12V R <sub>L</sub> =30Ω	lgт	10-80	mA
Latching current	Ig=1.2 Igт	lι	≤200	mA
Holding current	Iτ=1A	Ін	≤150	mA
Triggering gate voltage	V <sub>D</sub> =12V R <sub>L</sub> =30Ω	V <sub>GT</sub>	≤1.5	V
Non triggering gate voltage	V <sub>D</sub> =V <sub>DRM</sub> T <sub>J</sub> =125°C	$V_{\sf GD}$	≥0.25	V
Critical rate of rise of voltage	V <sub>D</sub> =2/3V <sub>DRM</sub> T <sub>J</sub> =125℃ Gate Open	dv/dt	≥1000	V/µs

# **Mechanical Characteristics**

Chip size	8.9mm×8.9mm	
Module size	29.7mm×18.2mm	
Terminal height	19.2mm	
Solder composition and melting point of DBC	Solder composition: Pb92.5%Sn5%Ag2.5% melting point>295°C.	



M					Dimensions			
	Millimeters		Inches					
Min	Тур	Max	Min	Тур	Max			
3.7	4.0	4.3	0.146	0.157	0.169			
10.3	10.8	11.3	0.406	0.425	0.445			
3.7	4.0	4.3	0.146	0.157	0.169			
0.2	0.5	0.8	0.008	0.020	0.031			
		19.2			0.756			
		6.2			0.244			
0.4	0.9	1.4	0.016	0.035	0.055			
3.9	4.4	4.9	0.154	0.173	0.193			
		6.0			0.236			
29.4	29.7	30.0	1.157	1.169	1.181			
17.9	18.2	18.5	0.705	0.717	0.728			
	3.7 10.3 3.7 0.2 0.4 3.9	3.7 4.0 10.3 10.8 3.7 4.0 0.2 0.5 0.4 0.9 3.9 4.4 29.4 29.7	3.7 4.0 4.3 10.3 10.8 11.3 3.7 4.0 4.3 0.2 0.5 0.8 19.2 6.2 0.4 0.9 1.4 3.9 4.4 4.9 6.0 29.4 29.7 30.0	3.7  4.0  4.3  0.146    10.3  10.8  11.3  0.406    3.7  4.0  4.3  0.146    0.2  0.5  0.8  0.008    19.2  6.2  0.04  0.9  1.4  0.016    3.9  4.4  4.9  0.154    6.0  29.4  29.7  30.0  1.157	3.7  4.0  4.3  0.146  0.157    10.3  10.8  11.3  0.406  0.425    3.7  4.0  4.3  0.146  0.157    0.2  0.5  0.8  0.008  0.020    19.2			

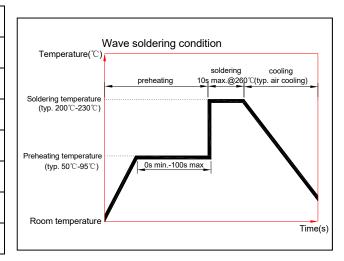






### **Soldering Process Requirements**

a. Hand soldering iron welding			
Soldering temperature	≤260℃		
Soldering time	≤10s		
b. Wave soldering (see figure at right)			
Preheating temperature	≤125℃		
Preheating time	≤100s		
Soldering temperature	≤260℃		
Soldering time	≤10s		



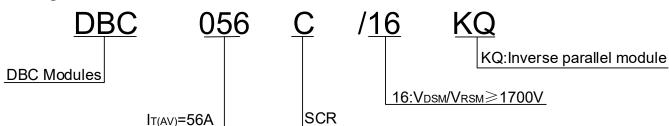
#### **Working Conditions**

- 1) No severe mechanical shock as impact and drop off in the process of transportation, storage and working of product.
- 2) Storage conditions Temperature: 5~40°C

Relative humidity: ≤45%

Storage time: 3 days for the open package; 3 months for the closed package

## **Ordering Information**



Information furnished in this document is believed to be accurate and reliable. However, JieJie Semiconductor Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it.

Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information.

This document is the 1.3rd version which is made in 20-Oct.-2023. This document supersedes and replaces all information previously supplied.

is registered trademarks of JieJie Semiconductor Co., Ltd. Copyright©2023 Semiconductor Co., Ltd. Printed All rights reserved.